EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("7151567").PN.	US-PGPUB; USPAT	OR	OFF	2009/01/19 13:37
S98	1386	(348/308).CCLS.	US-PGPUB; USPAT	OR	OFF	2009/01/15 16:50
S99	180	S98 and (reset near2 line) and (row near2 select$4 near2 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/15 16:51
S100	102	S98 and 4T	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/15 19:29
S101	24	((plural$3 or multipl $4) near2 capacitor) with (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 10:55
S102	1	(villecco.xa. or villecco.xp.) and ((multiple or plural $4) near4 capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 10:59
S103	53	((plural$3 or multipl $4) near2 capacitor) same (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:00
S104	7	S103 and "348"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:00
S105	6	(villecco.xa. or villecco.xp.) and ((siz $3 or number) near4 capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:05
S106	1544	((plural$3 or multipl $4) near2 capacitor) near4 output$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:07

S107	76	S106 and "348"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:07
S108	17	S107 and (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:08
S109	3888	348/294-309.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:07
S110	208	S109 and (simultaneous$3 near2 reset$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:07
S111	82	S109 and (global near2 reset$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:17
S112	55	(348/211.14).CCLS.	US-PGPUB; USPAT	OR	OFF	2009/01/16 16:12
S113	0	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and cathode and reset$4 and shift$4 and power and select$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:50
S114	0	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and reset$4 and shift$4 and power and select$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:51
S115	2	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and reset$4 and shift$4 and select$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:51

1/19/09 2:47:44 PM

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